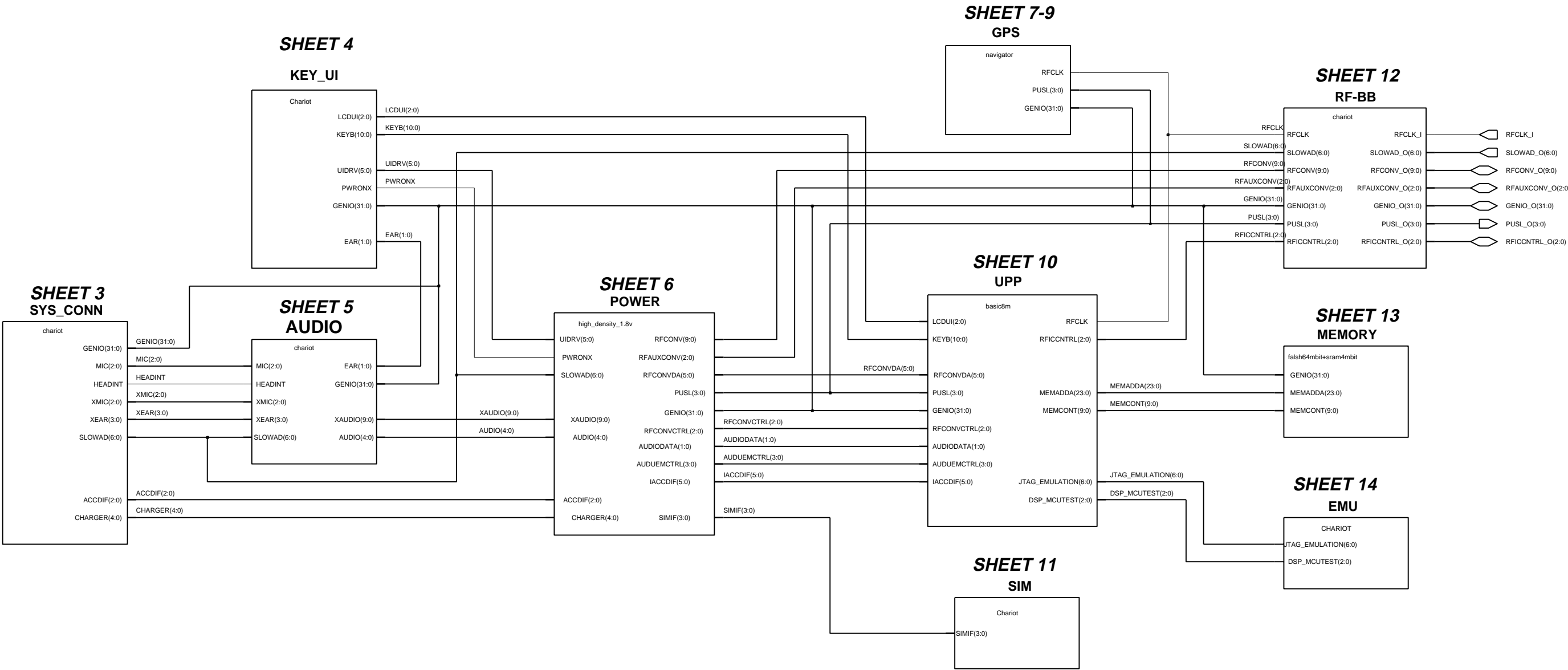
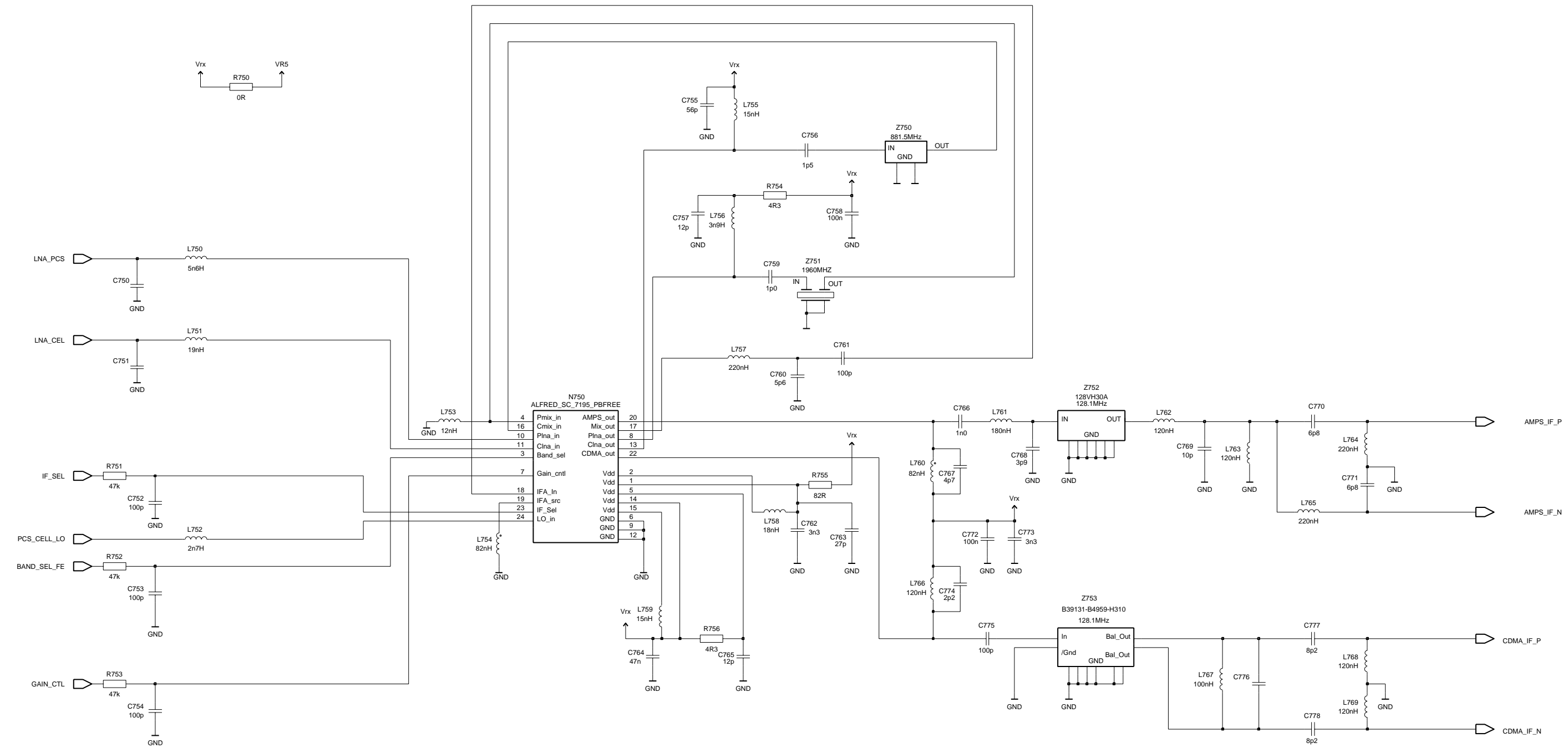


6015i/6016i Schematics

Baseband - Top

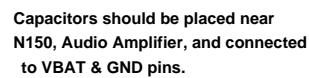


Front End

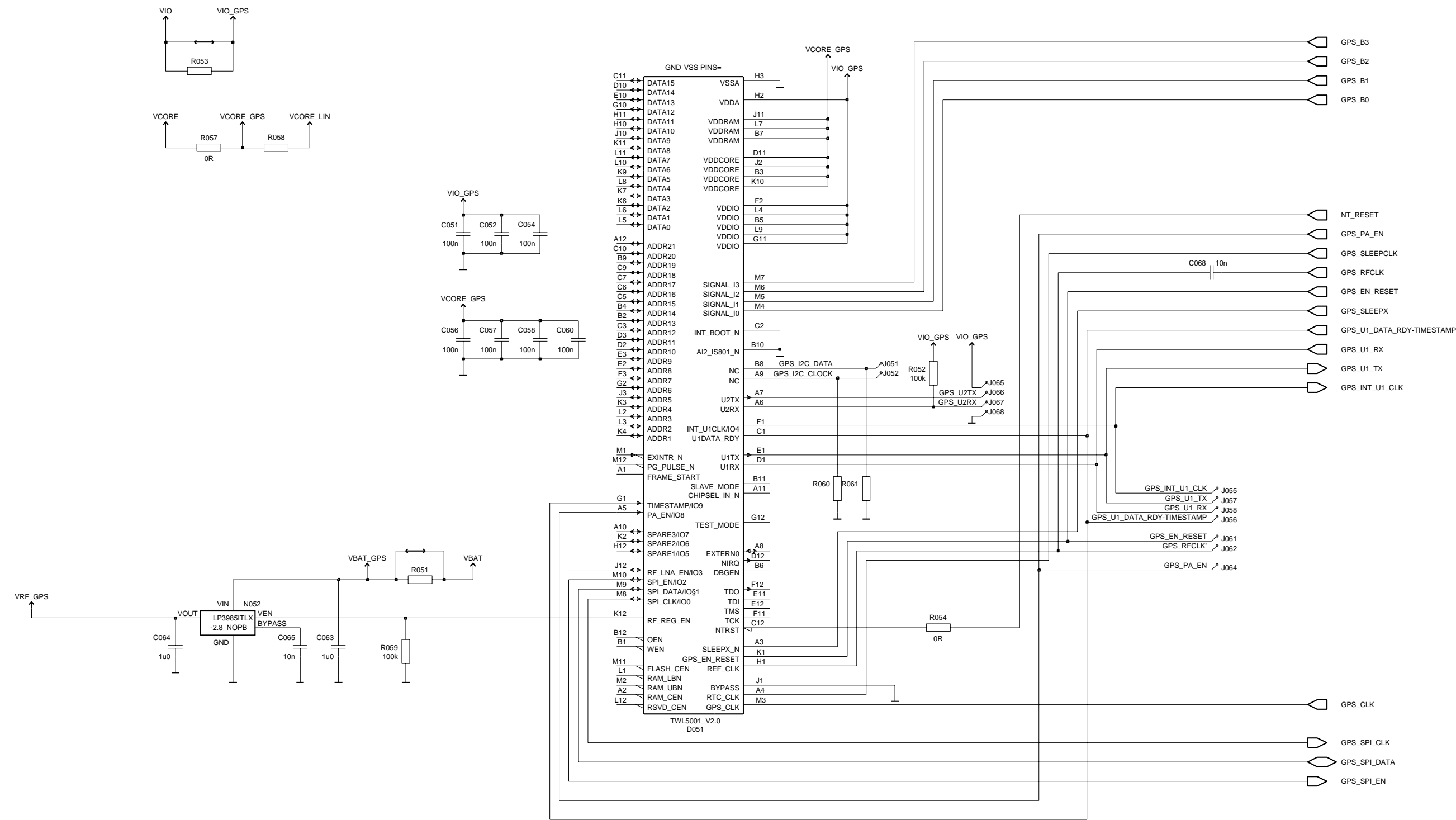


MODES	LOGIC INPUTS		
	BAND	GAIN_CTL	IF_SEL
CEL CDMA Hi Gain	0	1	0
CEL CDMA Lo Gain	0	0	0
PCS CDMA Hi Gain	1	1	0
PCS CDMA Lo Gain	1	0	0
AMPS Hi Gain	0	1	1
AMPS Lo Gain	0	0	1

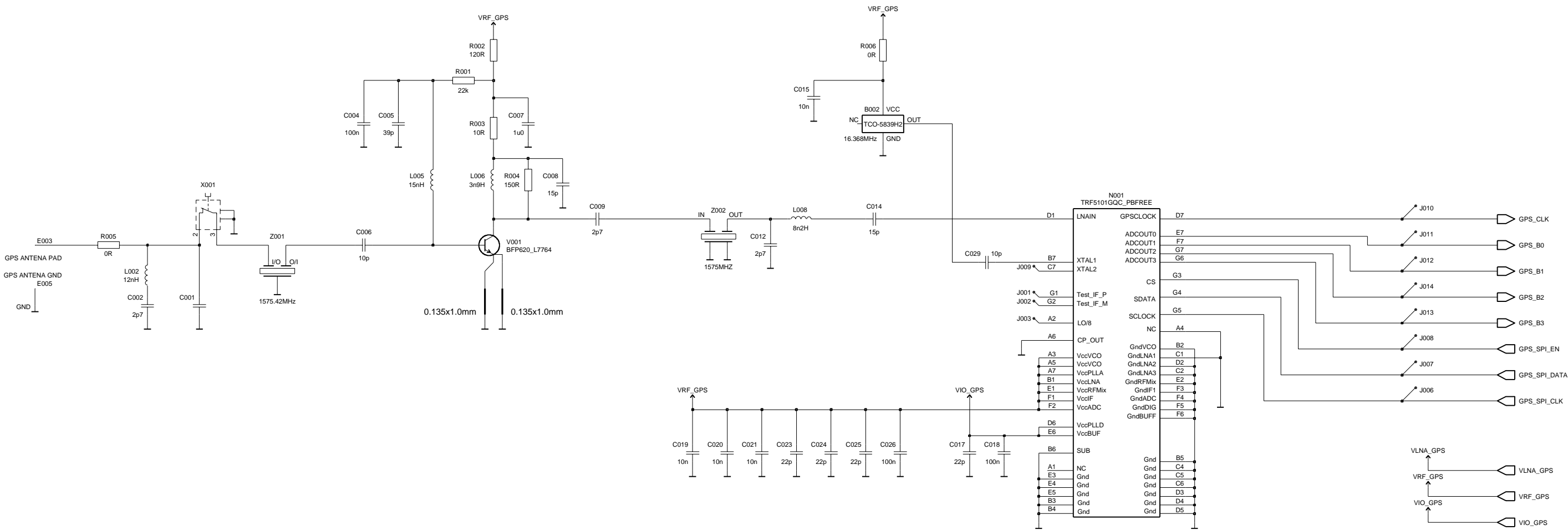
VARIANT DNI	
HDB91 800	HDC93 1900
C750 C759 L750 Z751	C751 C756 C766 C768 C769 C770 C771 L751 L761 L762 L763 L764 L765 Z750 Z752

[illegible]

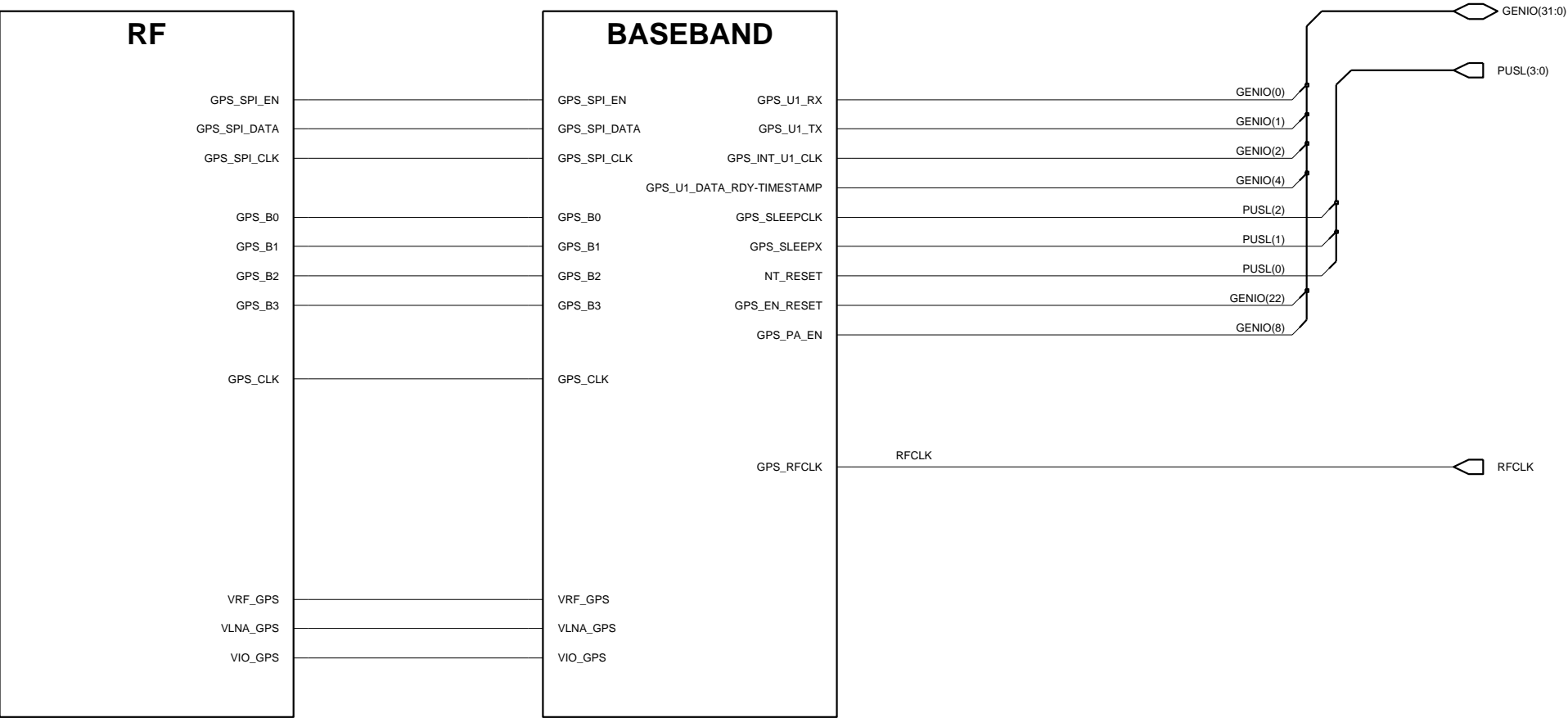
GPS Baseband



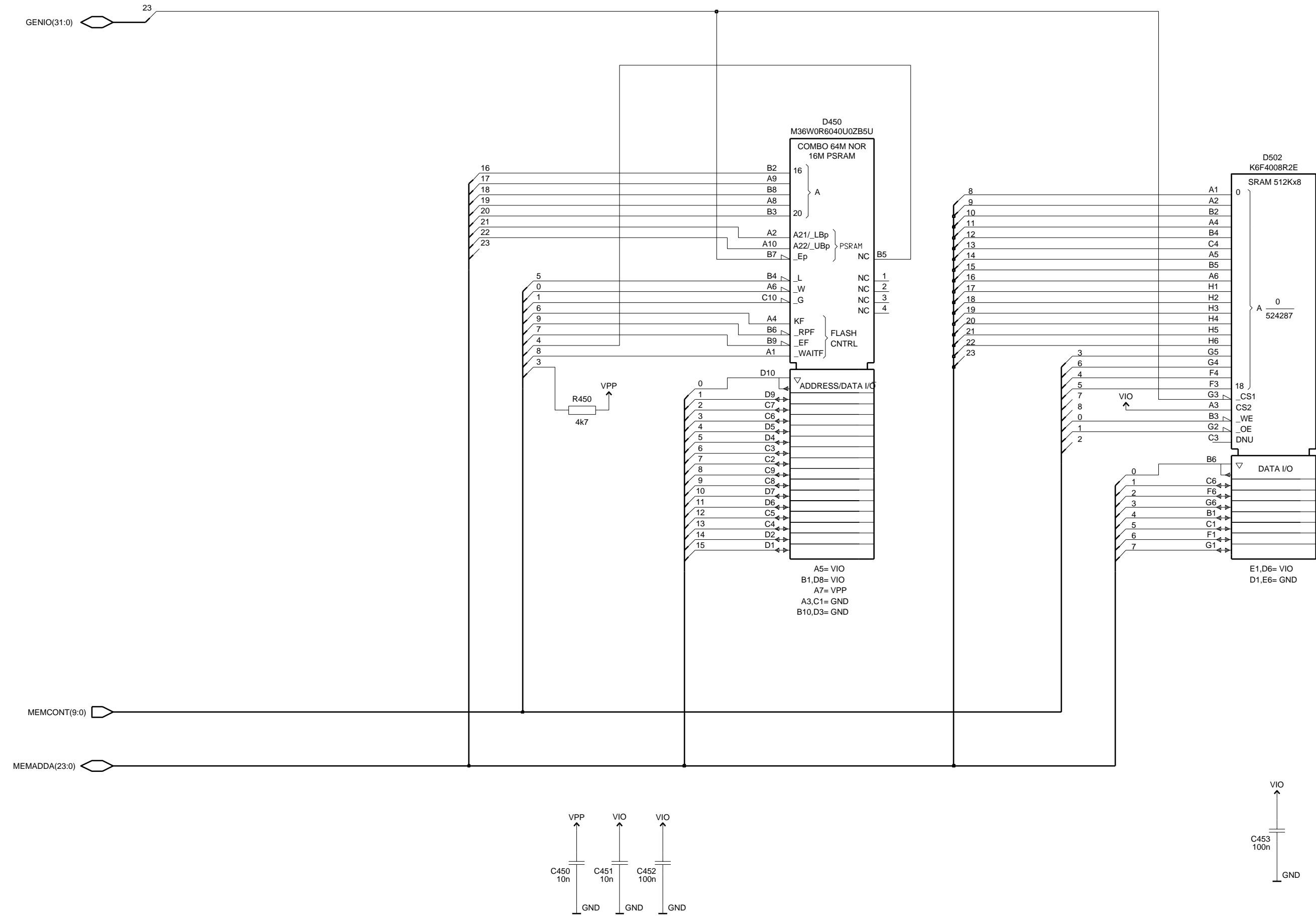
GPS RF



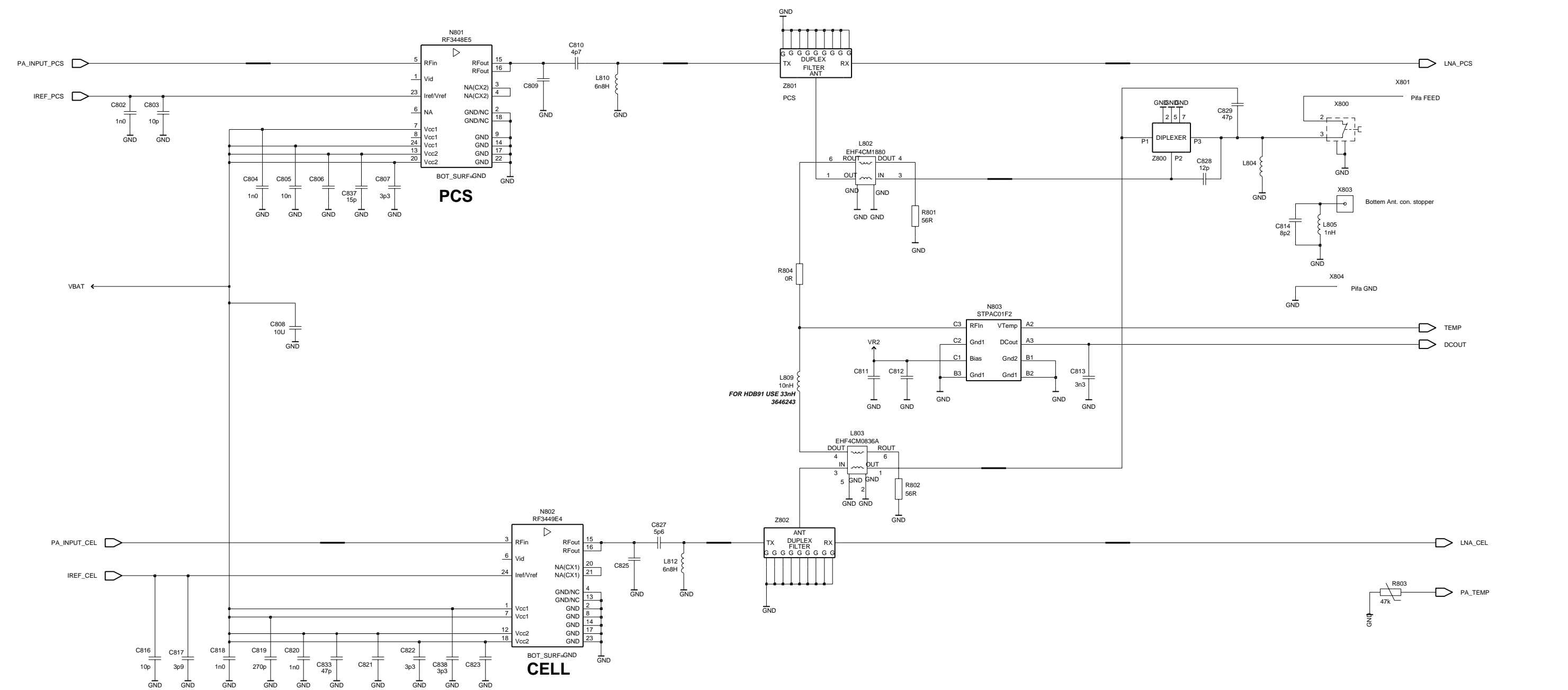
GPS RF BB Interface



Memory

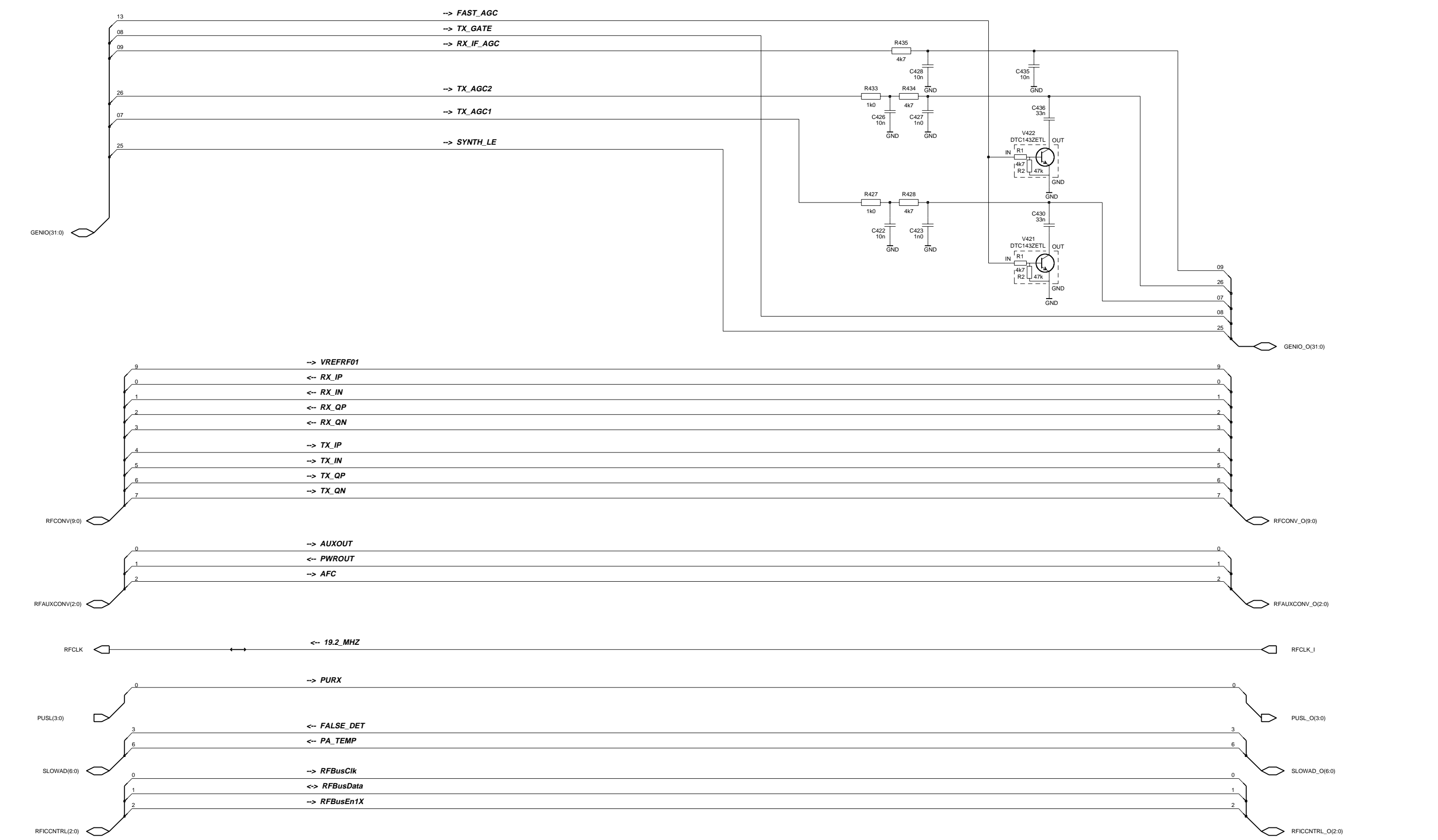


PA

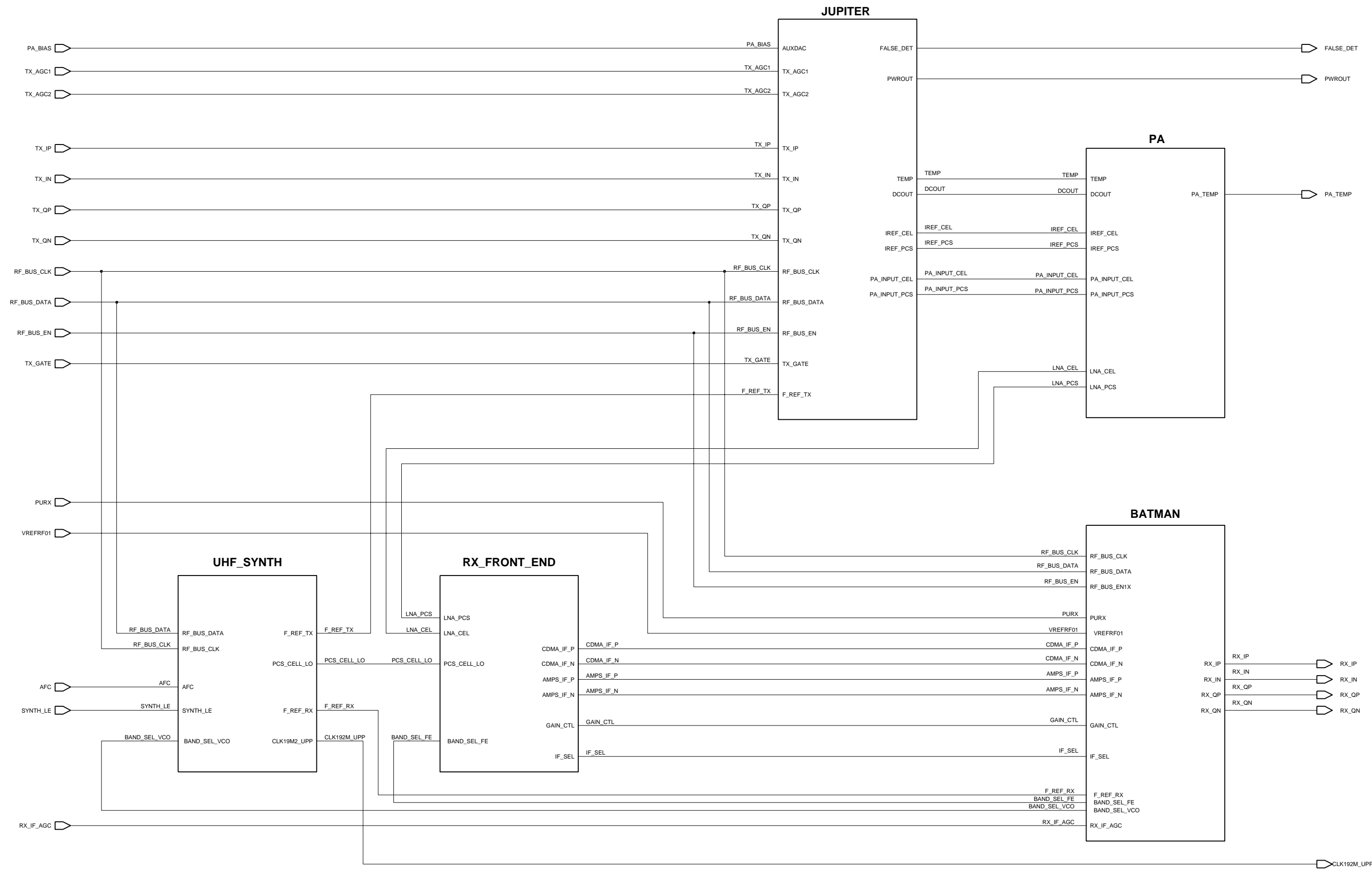


VARIANT DNI		
HDC91 DB	HDB91 800	HDC93 1900
C828	C802	C816
C829	C803	C817
	C804	C818
	C805	C819
	C806	C820
	C807	C821
	C809	C822
	C810	C823
	C828	C825
	C837	C827
	L802	C829
	L810	C833
	N801	C838
	R801	L803
	R804	L808
	Z800	L811
	Z801	R802
		Z800
		Z802

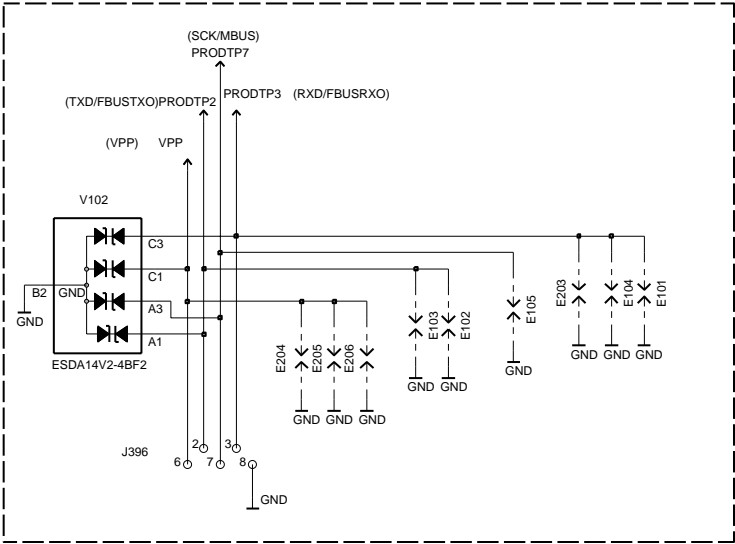
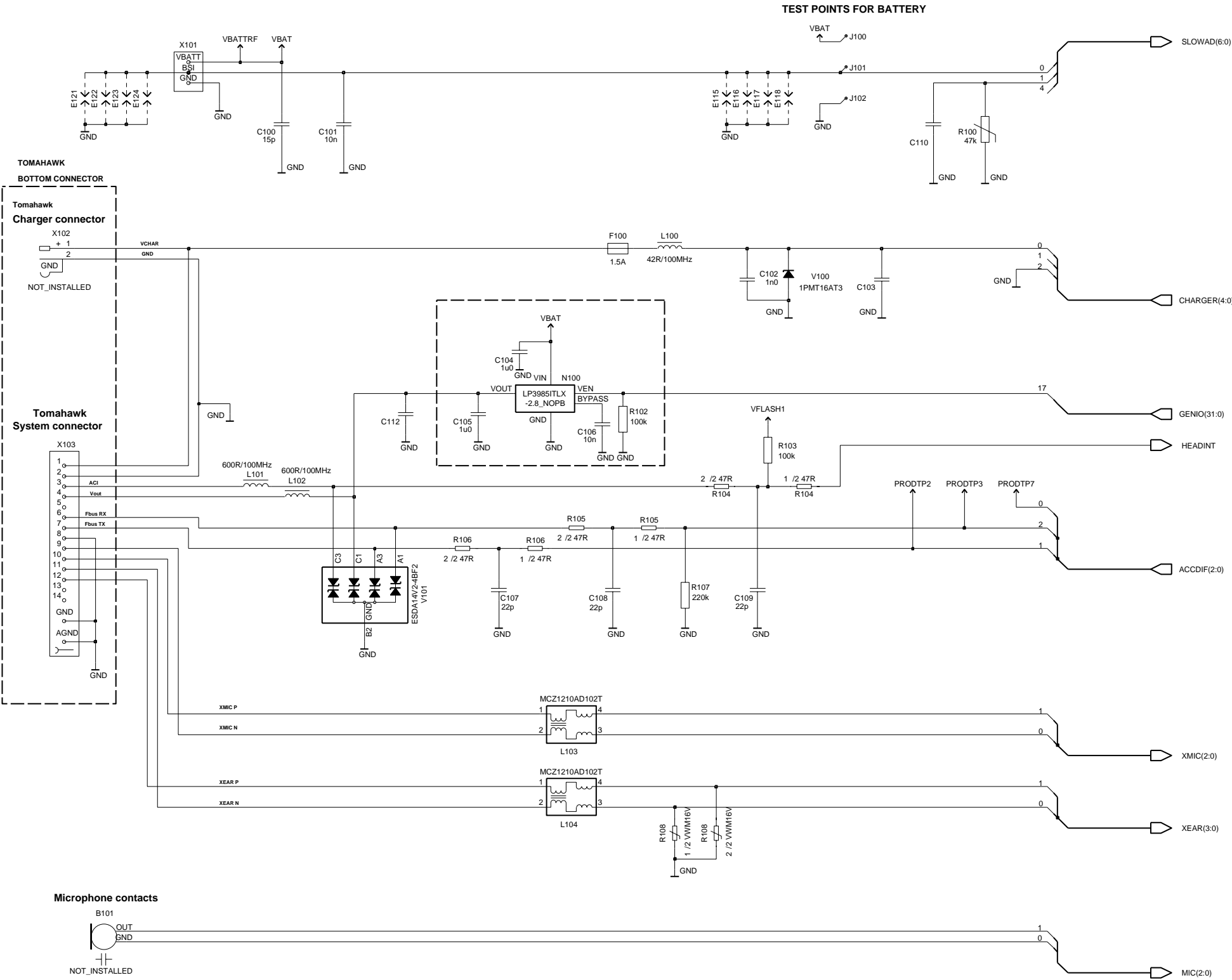
RF-BB Interface



RF - Top

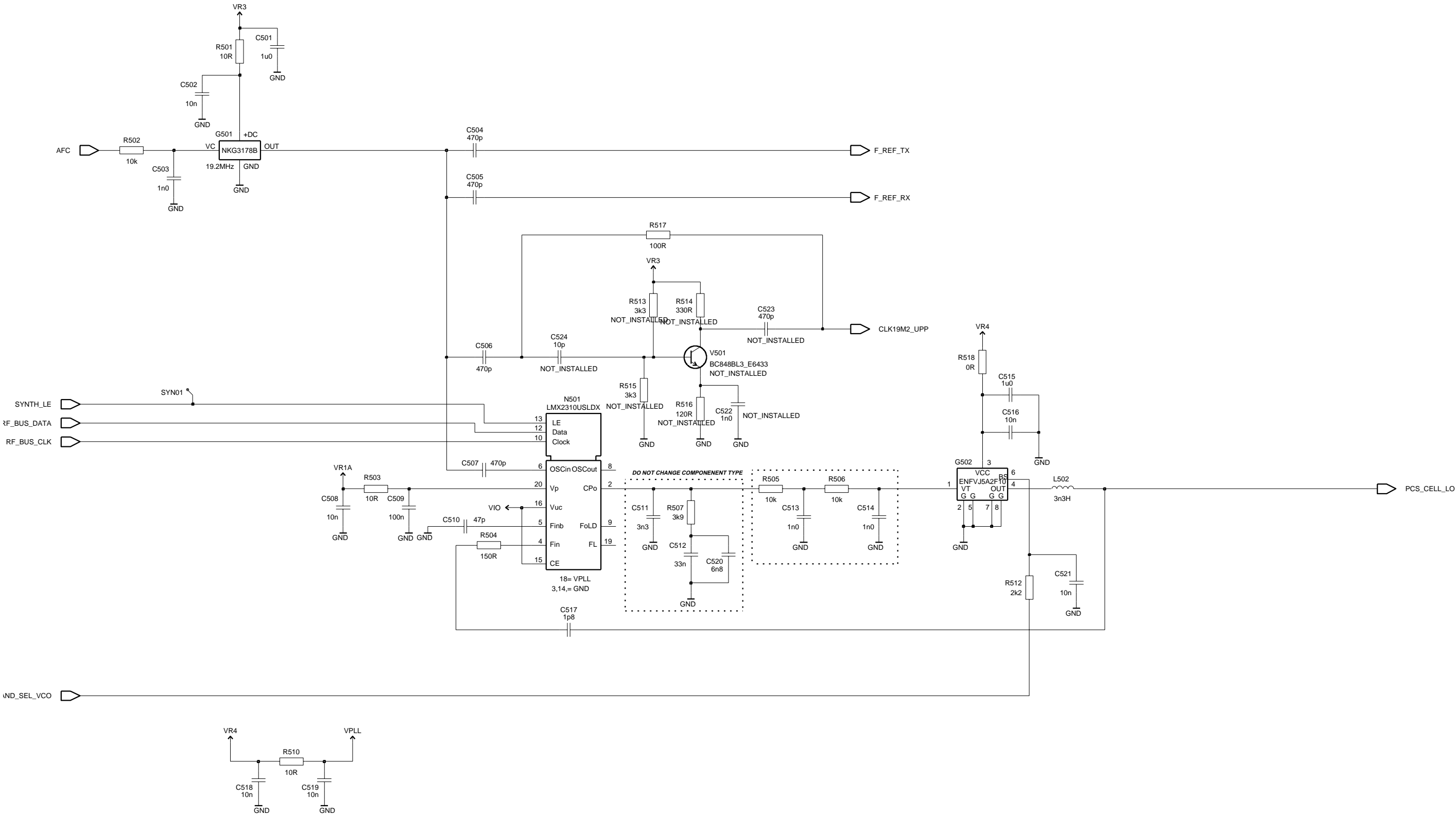


System Connector

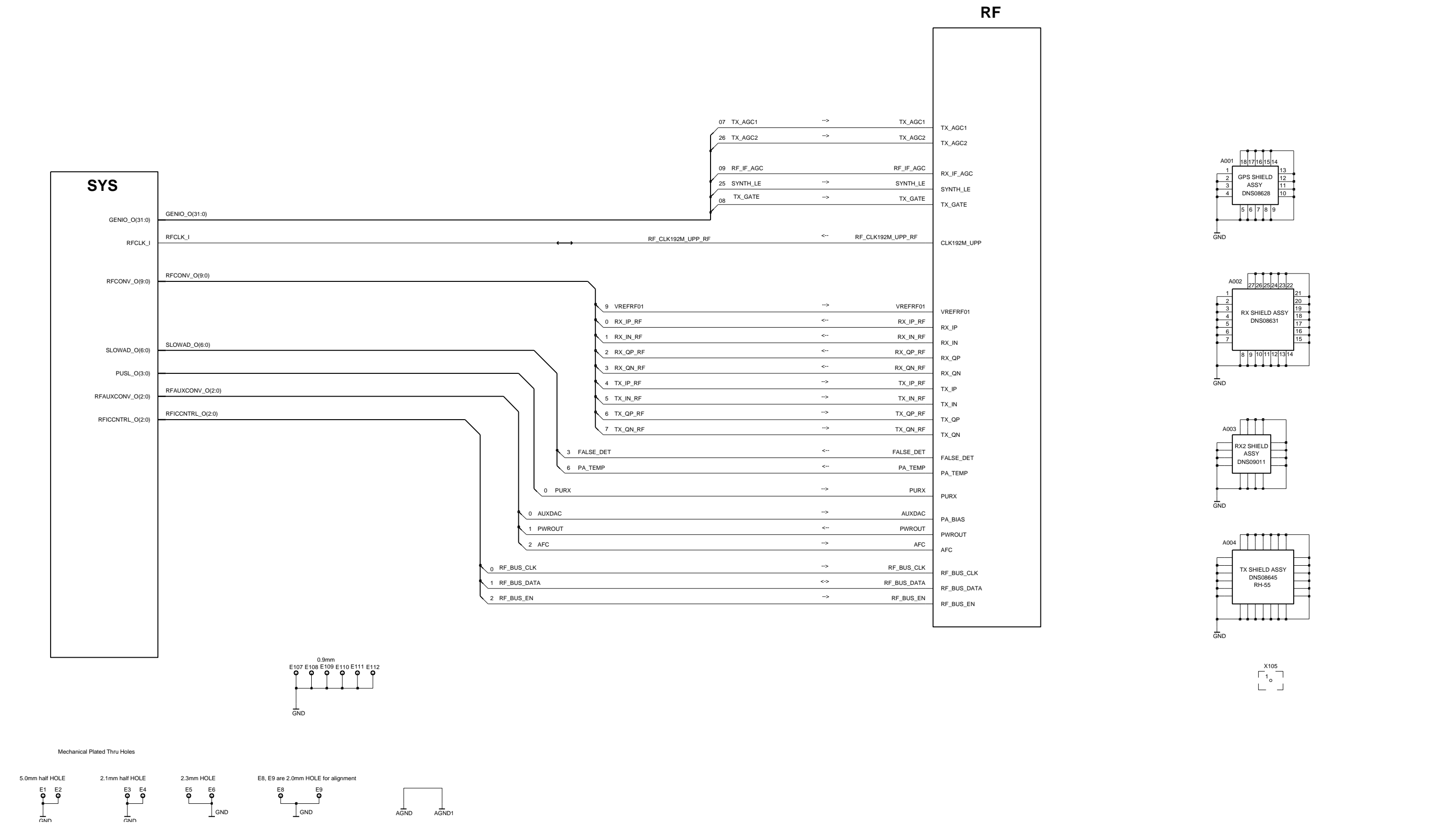


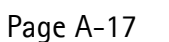
PROD TEST PATTERN

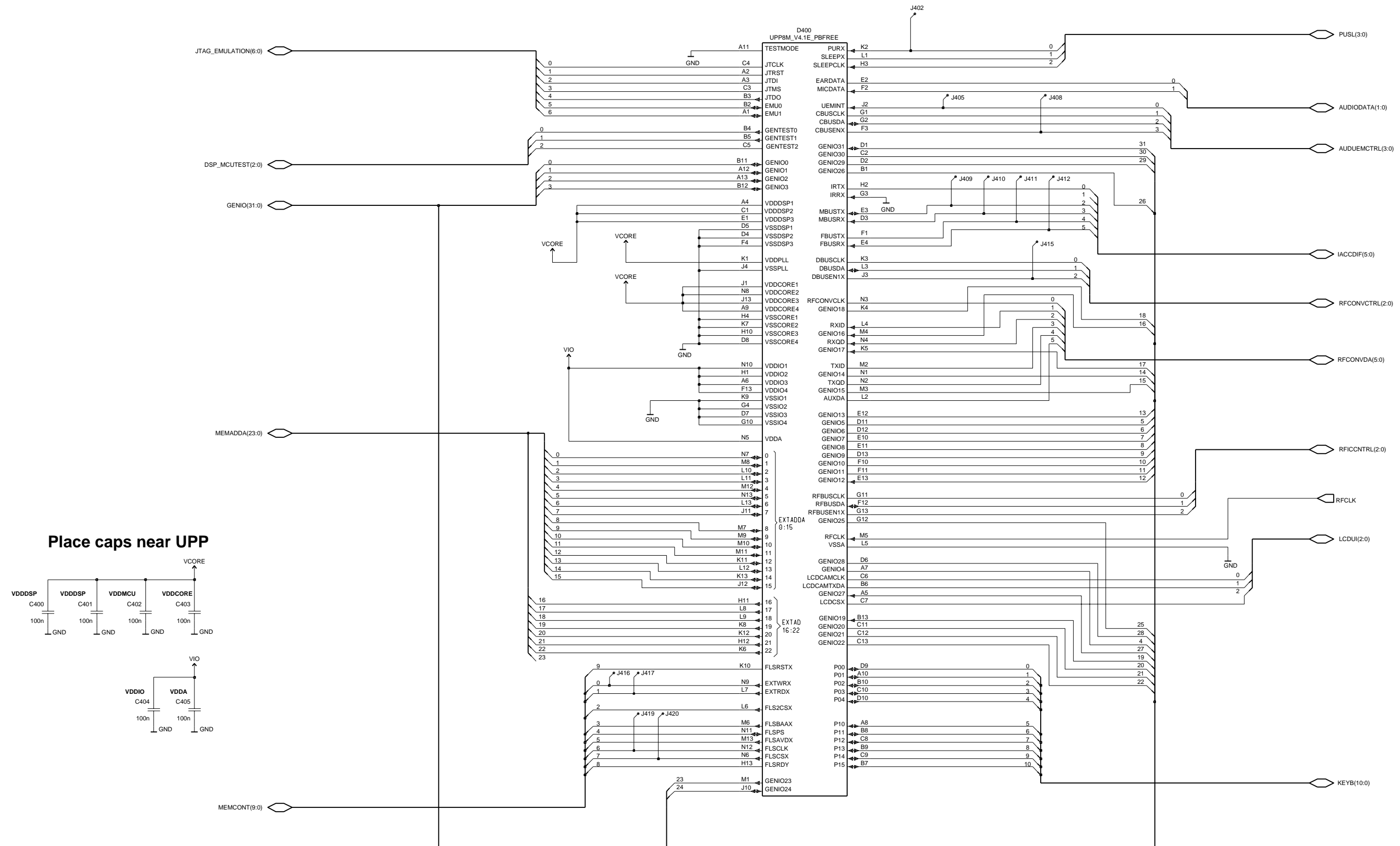
UHF Synthesizer



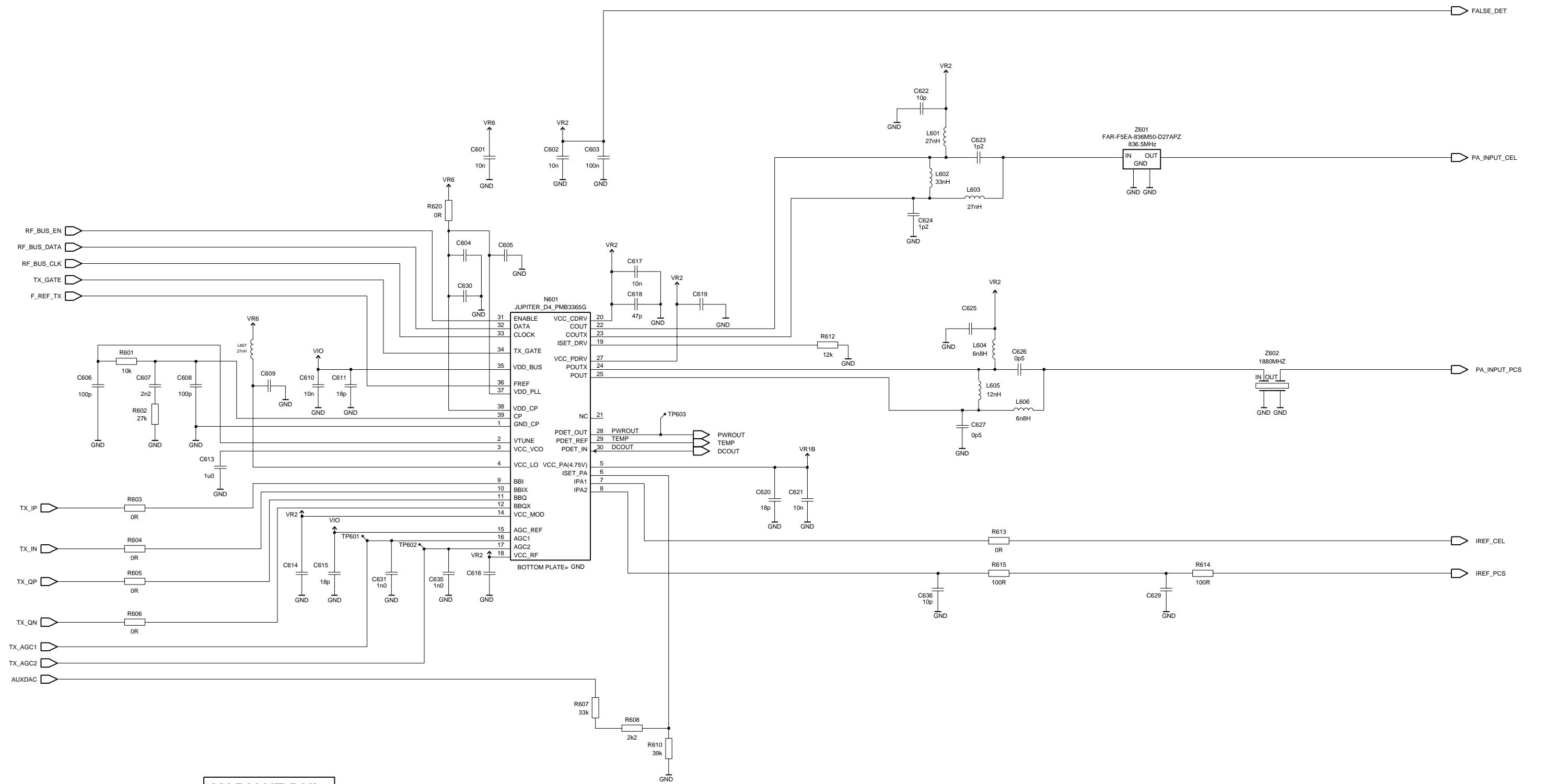
Top Level





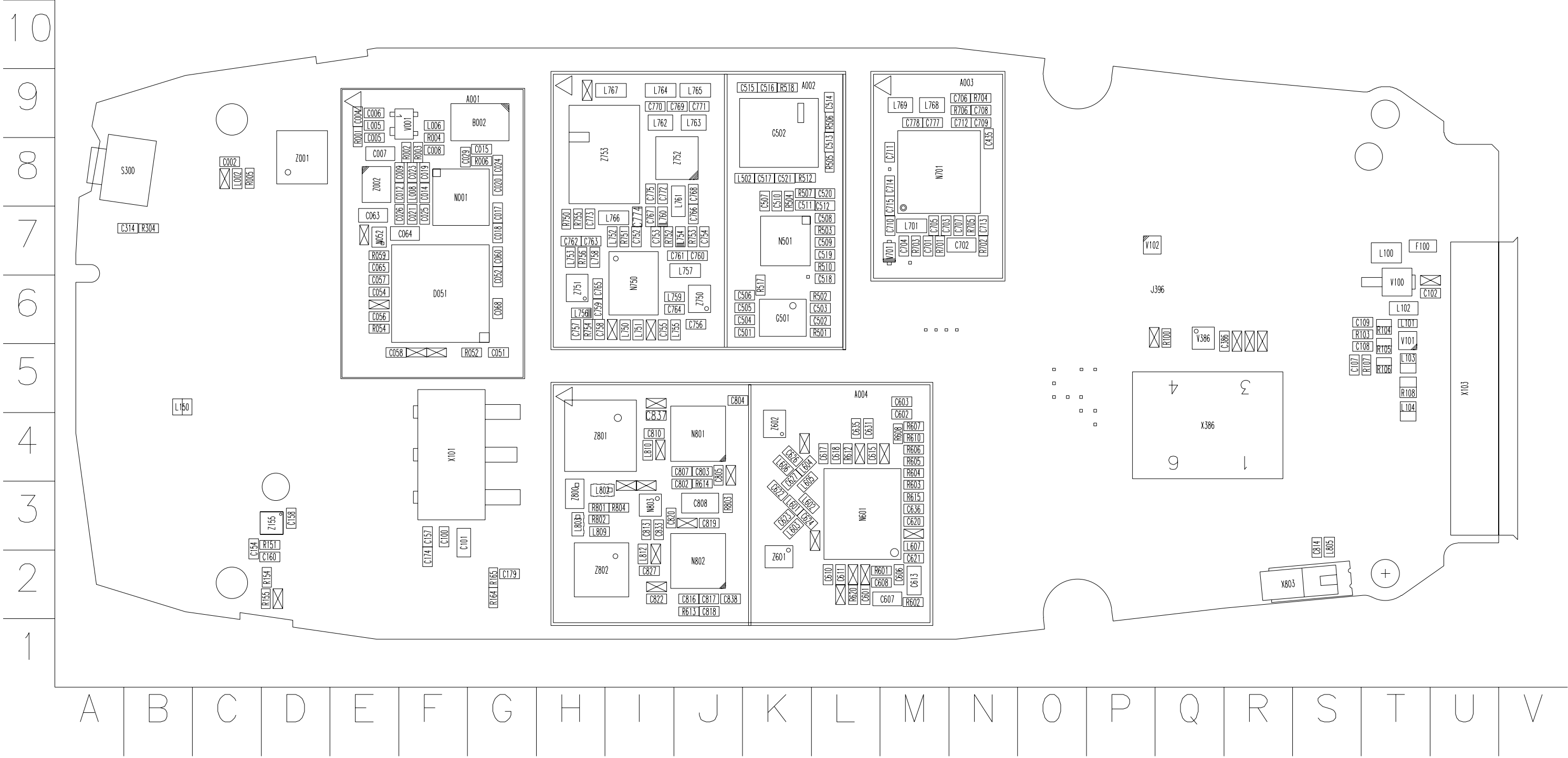


Jupiter

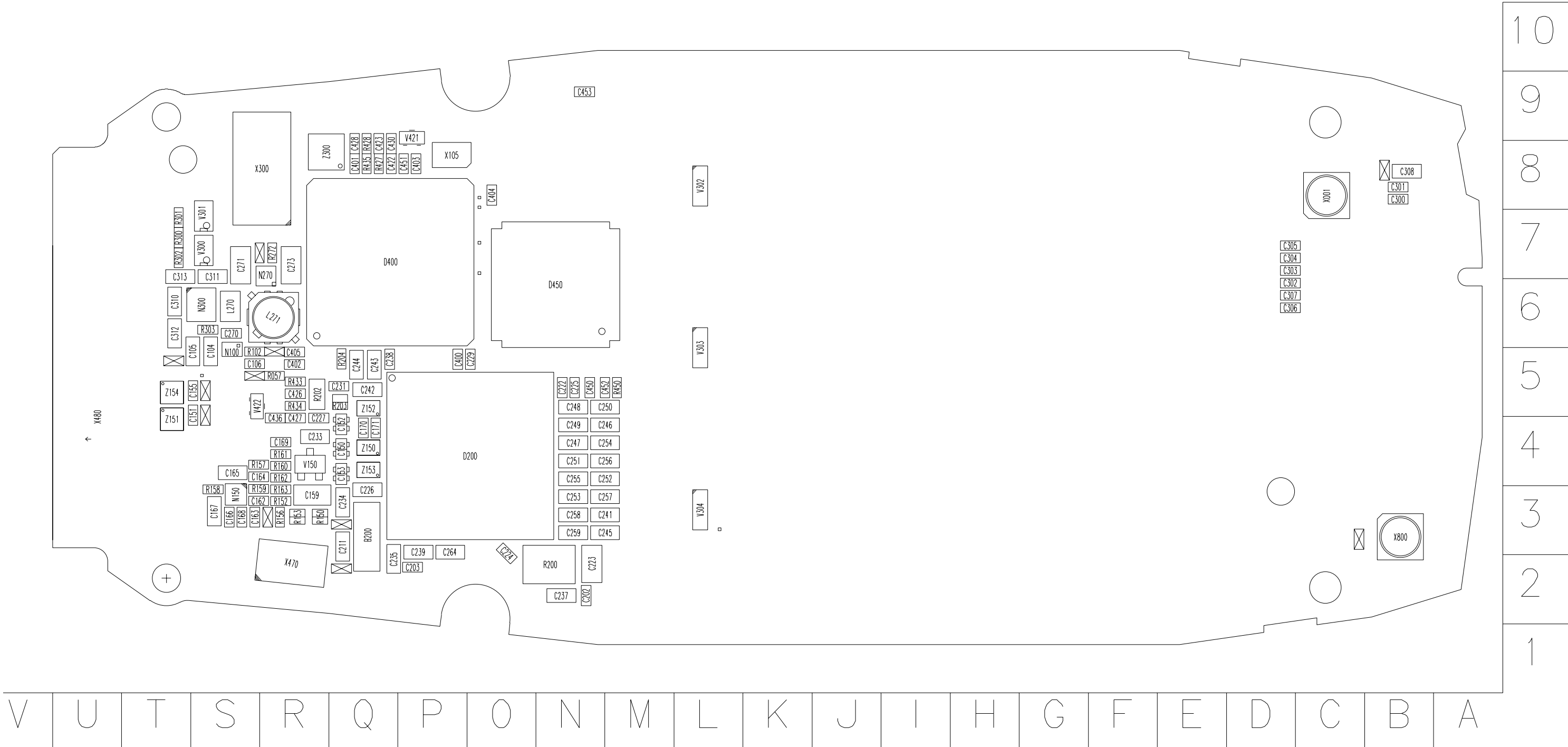


VARIANT DNI	
HDB91 800	HDC93 1900
C625	C622
C626	C623
C627	C624
C629	L601
L604	L602
L605	L603
L606	R613
R614	Z601
Z602	

6015i/6016i — 0202277 Component Layout — Top

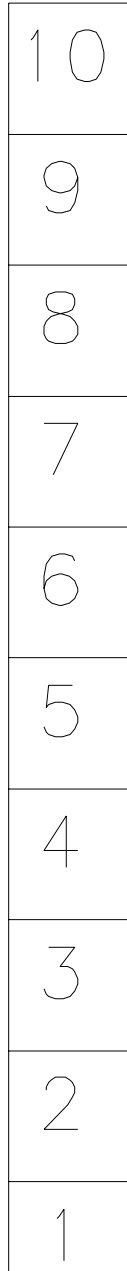


6015i/6016i — 0202277 Component Layout — Bottom



The image shows a detailed PCB layout for a 100MHz PLL circuit. The layout is on a rectangular board with dimensions 100mm x 100mm. It features a central IC (PLL) with various components around it, including capacitors, resistors, and inductors. The components are labeled with their values and units. The layout is organized into several functional blocks, such as the PLL core, reference frequency divider, and output filter. The board has a grid system with letters A-V and numbers 1-10. The layout is a top-down view showing the placement of components and the routing of signals.

V	U	T	S	R	Q	P	O	N	M	L	K	J	I	H	G	F	E	D	C	B	A
---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---	---



This page intentionally left blank.